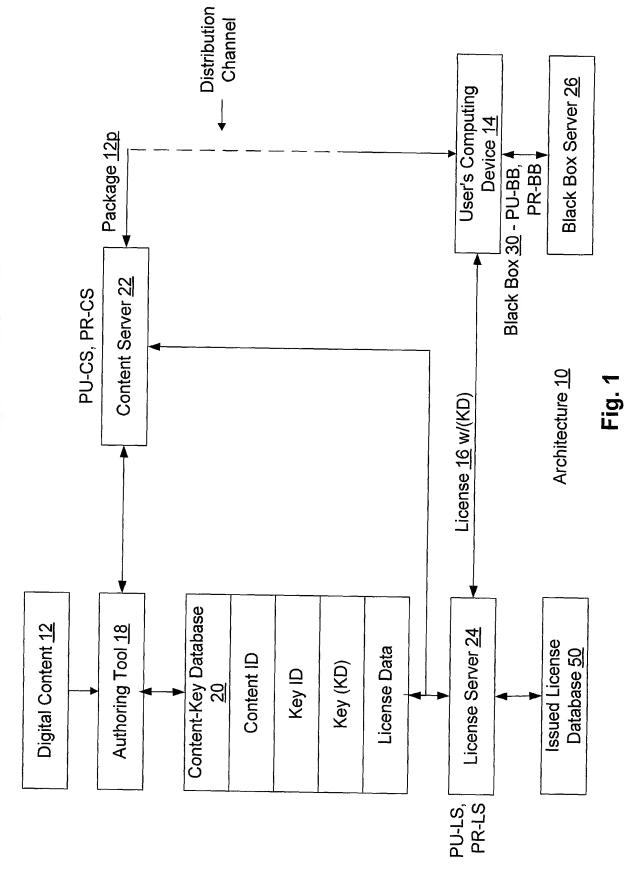
Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 1 of 18

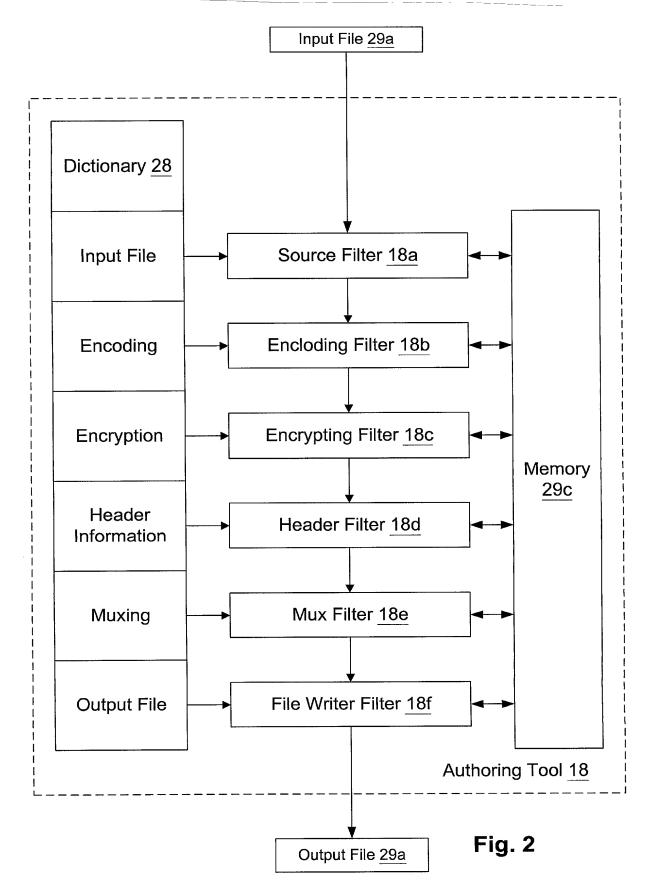


Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 2 of 18



the first first form and the first f

Docket No.: MSFT-0312

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 3 of 18

License 16

Content ID

DRL 48 or KD (DRL 48)

PU-BB (KD)

S (PR-LS)

S (PR-LS)

-<u>ig</u>. 8

Digital Content Package

12p

KD (Digital Content 12)

Content ID

Key ID

License Acquisition Info

KD (PU-CS) S (PR-CS)

<u>.</u> ලි

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 4 of 18

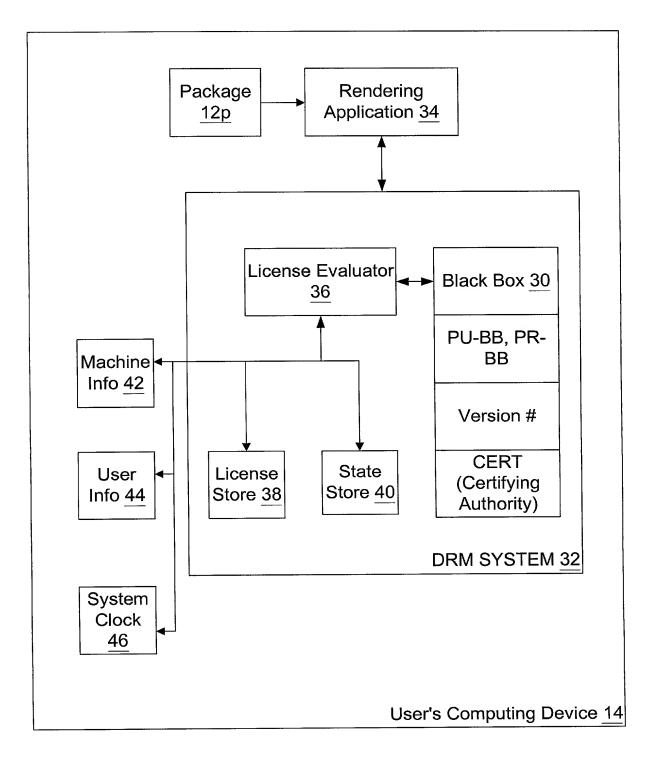


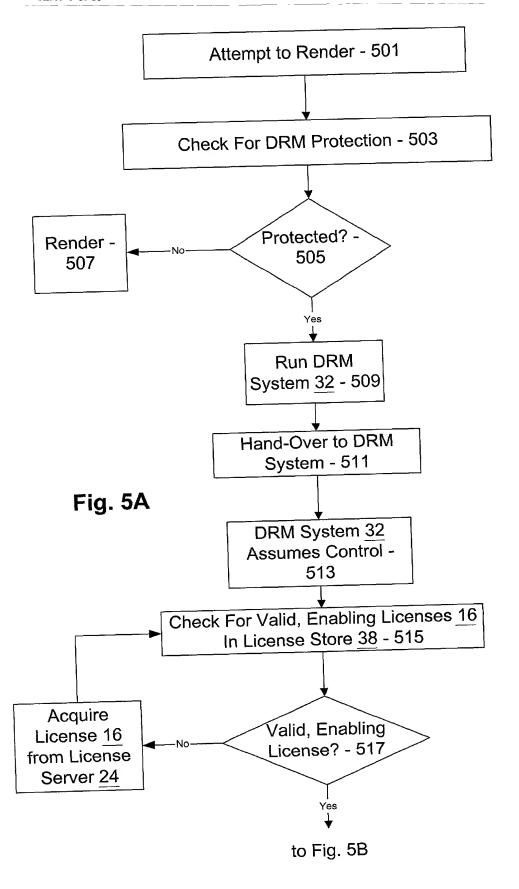
Fig. 4

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 5 of 18



Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 6 of 18

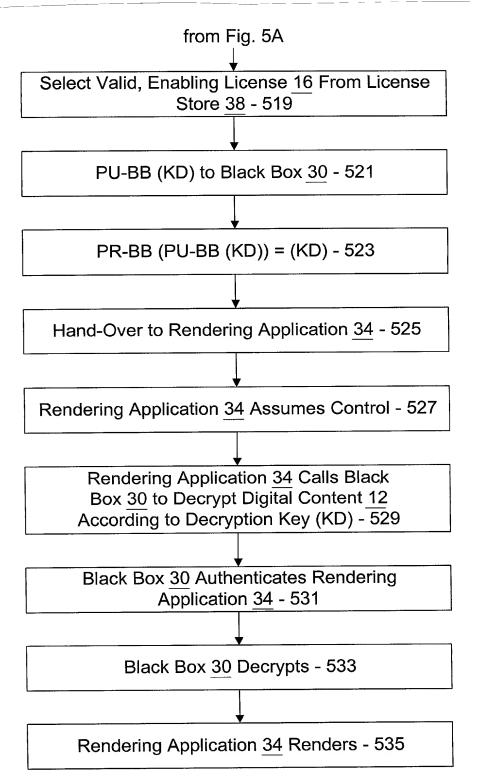


Fig. 5B

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 7 of 18

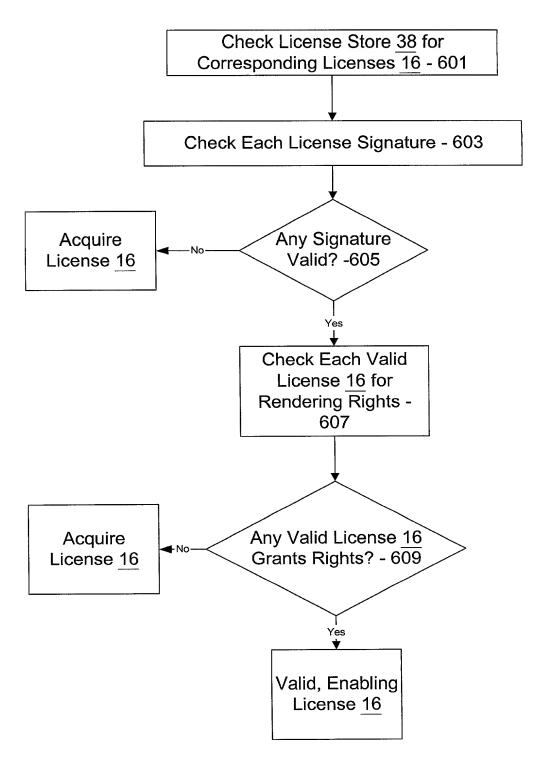


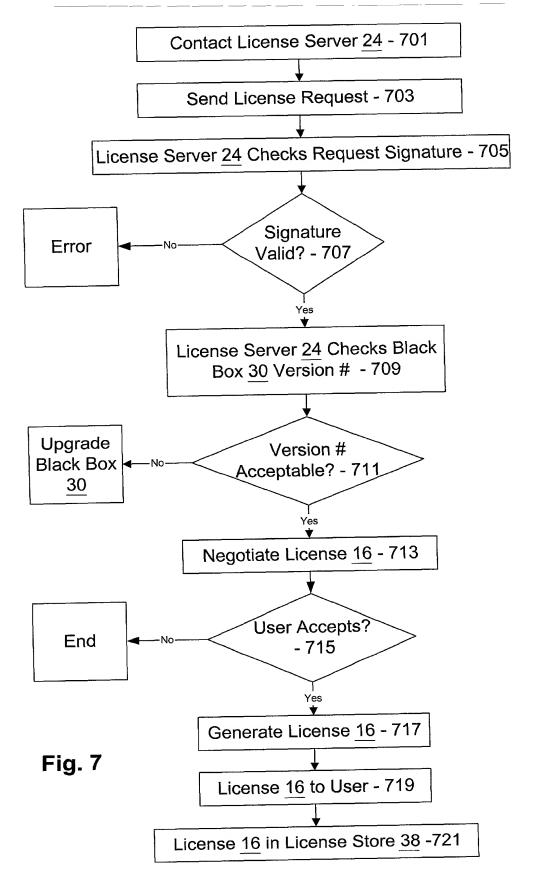
Fig. 6

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 8 of 18



Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 9 of 18

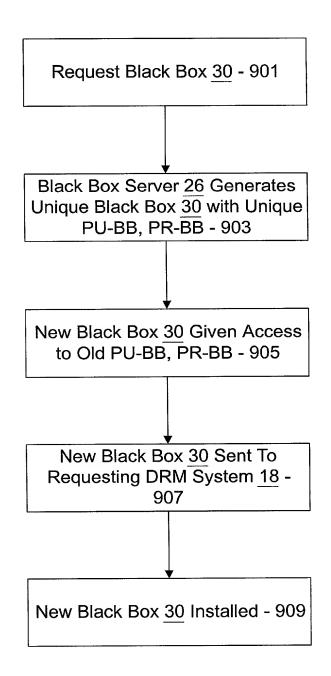


Fig. 9

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 10 of 18

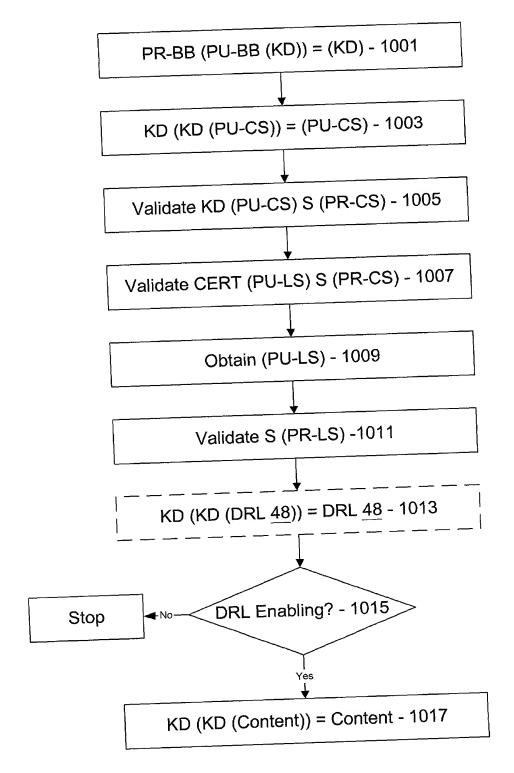


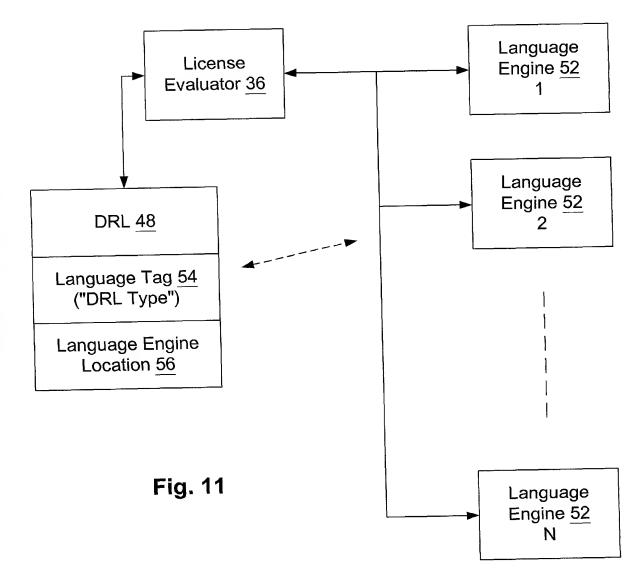
Fig. 10

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

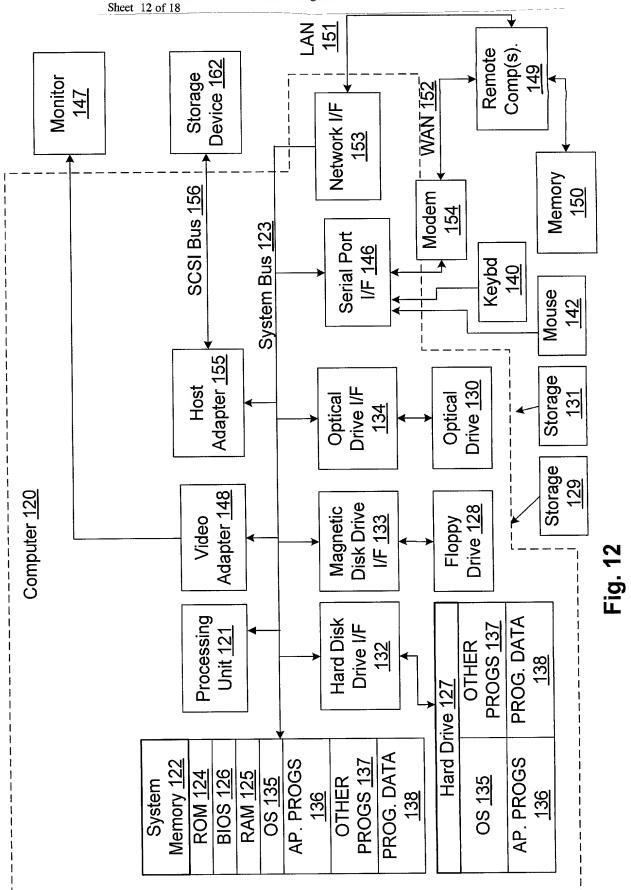
Sheet 11 of 18



Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England



Inventor(s): Marcus Peinado and Paul England Sheet 13 of 18 **CONTENT 12, LICENSE 16** DRM SYSTEM 32 COMPUTER 60 **CONTENT 12, LICENSE 16** DRM SYSTEM 32p SECURITY KERNEL 68 CHOOSER AP. 72c CACHE 74 SECURE PROCESSOR 64 AP. 72 CHOOSER VALUE AP. 72 76 SECURITY KERNEL **CPU KEY 66** 68 STORAGE SPACE 70 PORTABLE DEVICE 62

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

Docket No.: MSFT-0312

System On A Computing Device

Fig. 13

Marie Marie

Docket No.: MSFT-0312

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 14 of 18

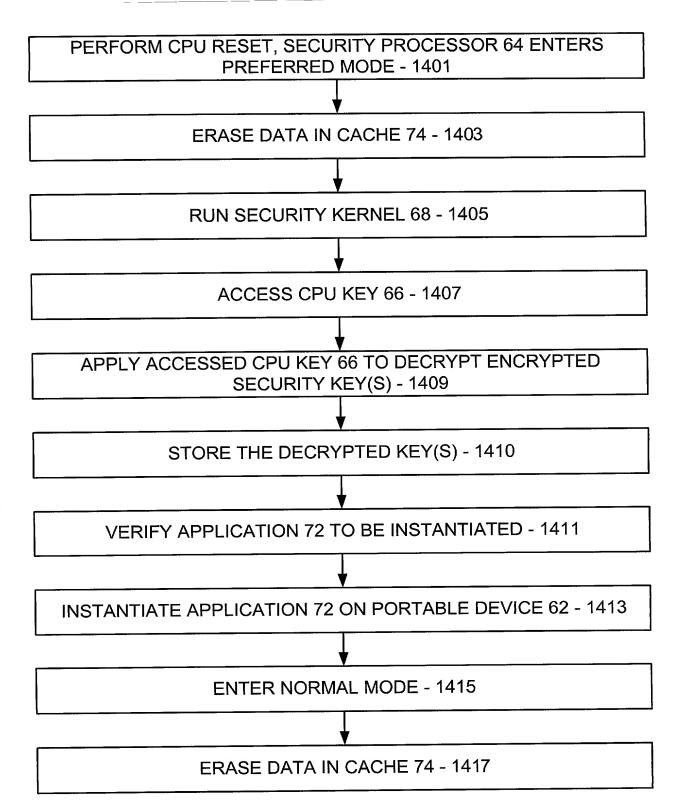


FIG. 14

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 15 of 18

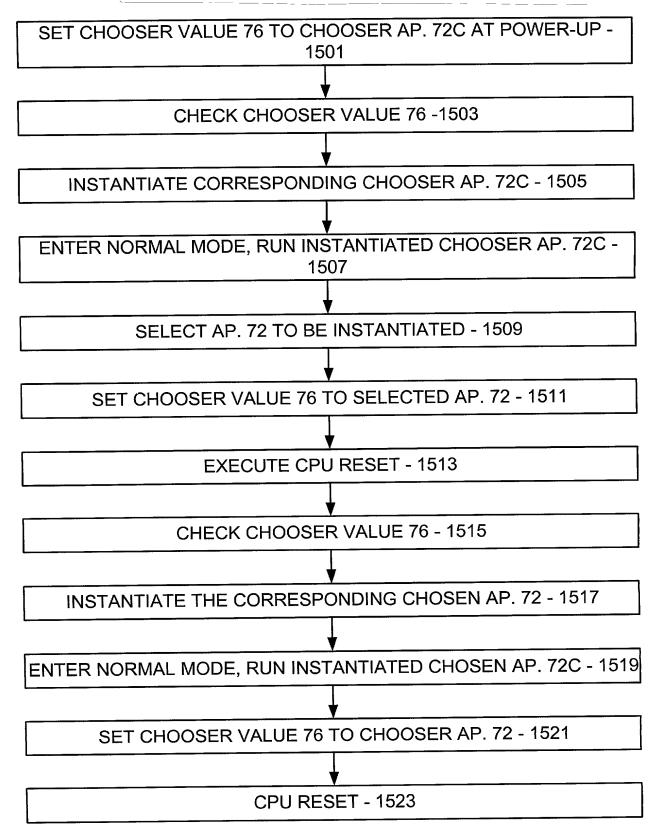


FIG. 15

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 16 of 18

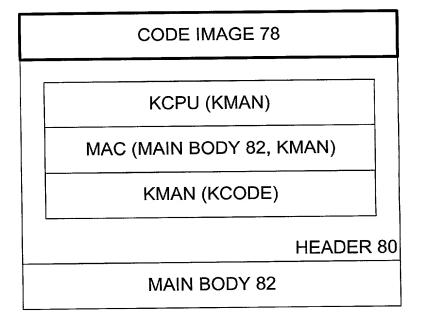


Fig. 16

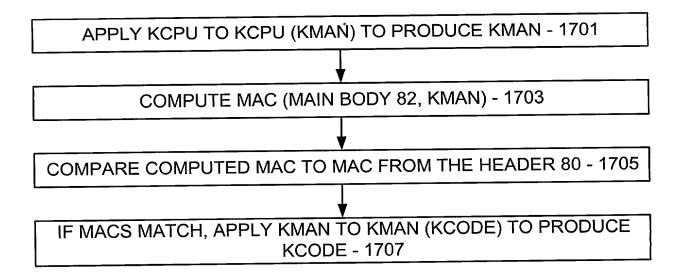


FIG. 17

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 17 of 18

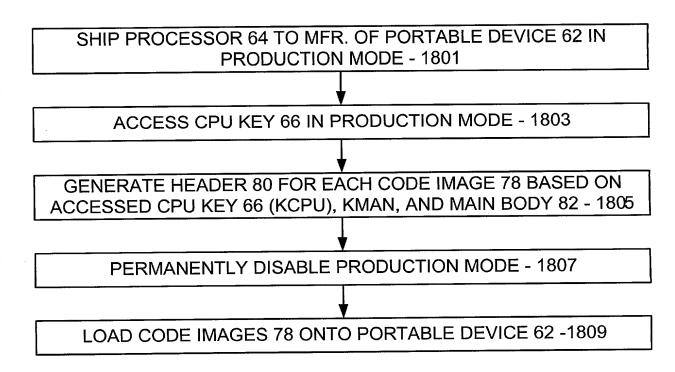


FIG. 18

Title: Secure Processor Architecture For Use With A Digital Rights Management (DRM)

System On A Computing Device

Inventor(s): Marcus Peinado and Paul England

Sheet 18 of 18

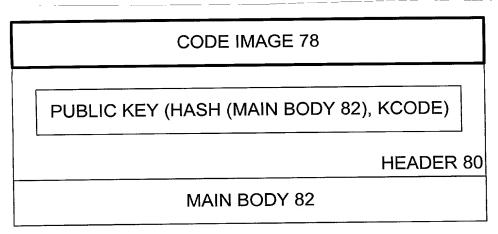


Fig. 19

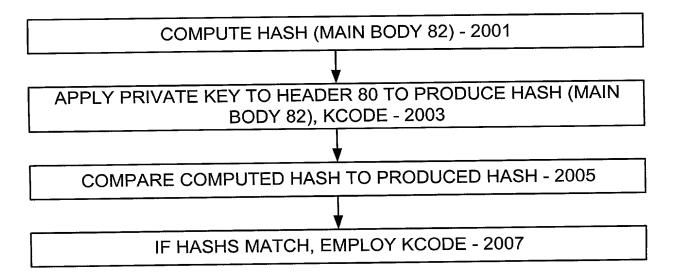


FIG. 20

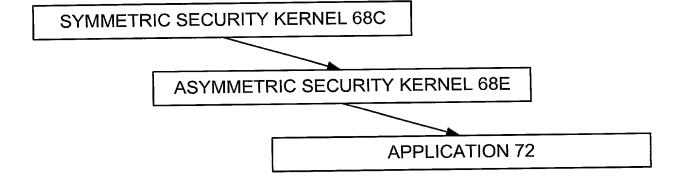


FIG. 21